« Software Verification by Abstract Interpretation and the ASTRÉE Static Analyzer »

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Seminar, SUNY SB CS, 18/01/2008 $-1 -$ © P. Cousot $\mathcal{O}_{\mathbb{R} \mathbb{C}}$ \math

Contents

Abstract

Abstract interpretation is a theory of sound approximation of the behavior of dynamic systems, in particular the semantics of programming languages. This is the formal basis for automatic correctness proofs by static analysers considering an over-approximation of the set of all possible executions of the program. Contrary to bug-finding methods (e.g. by test, bounded model-checking or error pattern search), no potential error is ever omitted. Hence the proof of satisfaction of a specification is always mathematically valid. Contrary to refinement-based methods, termination is always guaranteed. However, by undecidability of such proofs, the abstraction may yield false alarms whenever a synthesized inductive argument (e.g. a loop invariant) is too weak to make the proof. In this case, some executions considered in the abstract, that is in the over-approximation, might lead to an error while not corresponding to a concrete, that is actual, execution. All the difficulty of the undecidable verification problem is therefore to design safe/sound over-approximations that are coarse enough to be effectively computable by the static analyzer and precise enough to avoid false alarms (the errors leading to true alarms can only be eliminated by correcting the program that does not satisfy the specification).

After a brief introduction to abstract interpretation, we will present the AsTRÉE static analyser (www.astree.ens.fr) for proving the absence of runtime errors (such as buffer overrun, dangling pointer, division by zero, float overflow, modular integer arithmetic overflow, . . .) in real-time synchronous control/command C applications. The ASTRÉE static analyser uses generalist abstractions (like intervals, octagons, decision trees, symbolic execution, etc) and abstractions for the specific application domain (to cope with filters, integrators, slow divergences due to rounding errors, etc). Since 2003, these domain-specific abstractions allowed for the verification of the absence of runtime errors in several large avionic software, a world première.

Bugs Now Show-Up in Everyday Life

- Bugs now appear frequently in everyday life (banks, cars, telephones, . . .)
- Example (HSBC bank $ATM¹$ at 19 Boulevard Sébastopol in Paris, failure on Nov. 21^{st} 2006 at 8:30 am):

The Complexity of Software Design

- The design of complex software is difficult and economically critical
- Example (www.designnews.com/article/CA6475332.html):

"Boeing Confirms 787 Delay, Fasteners, Flight Control Software Code Blamed John Dodge, Editor-in-Chief – Design News, September 5, 2007

Boeing officials confirmed today that a fastener shortage and problems with flight control software have pushed "first flight" of the Boeing 787 Dreamliner to sometime between mid-November and mid-December 2.

... The software delays involve Honeywell Aerospace, which is responsible for flight control software. The work on this part of the 787 was simply underestimated, said Bair.**"**

² Bill Rigby of Reuters announced that Boeing delays 787 by 3 months on Wed Jan 16, 2008 12:37pm EST.

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A Strong Need for Software Better Quality

– Poor software quality is not acceptable in safety and mission critical software applications.

– The present state of the art in software engineering does not offer sufficient quality garantees

The Security of Complex Software

- Complex software is subject to security vulnerabilies
- Example (www.wired.com/politics/security/news/2008/01/dreamliner_security)

"FAA: Boeing's New 787 May Be Vulnerable to Hacker Attack Kim Zetter, freelance journalist in Oakland, CA, Jan. 4, 2008

Boeing's new 787 Dreamliner passenger jet may have a serious security vulnerability in its onboard computer networks ...

According to the FAA document published in the Federal Register (mirrored at Cryptome.org), the vulnerability exists because the plane's computer systems connect the passenger network with the flight-safety, control and navigation network. It also connects to the airline's business and administrative-support network, which communicates maintenance issues to ground crews.

Tool-Based Software Design Methods

- New tool-based software design methods will have to emerge to face the unprecedented growth and complexification of critical software
- E.g. FCPC (Flight Control Primary Computer)
	- A220: 20 000 LOCs,
	- A340 (V1): 130 000 LOCS
	- A340 (V2): 250 000 LOCS
	- A380: 1.000.000 LOCS
	- A350: static analysis to be integrated in the software production

Static Analysis

A static analyzer is a program that

- takes as input:
	- a program P (written in some given programming language $\mathbb P$ with a given semantics $\mathfrak{S}_{\mathbb P}$)
	- a specification S (implicit S P) or written in some specification language S with a given semantics $\mathfrak{S}_{\mathbb{S}}$)
- always terminates and delivers automatically as output:
	- a diagnosis on the validity of the program semantics with respect the specification semantics

Validation/Formal Methods

- Bug-finding methods : unit, integration, and system testing, dynamic verification, bounded model-checking, error pattern mining, . . .
- Absence of bug proving methods : formally prove that the semantics of a program satisfies a specification
	- theorem-proving & proof checking
	- model-checking
	- static analysis
- In practice : complementary methods are used, very difficult to scale up

Difficulties of Static Analysis

- automatic + infinite state + termination \implies undecidable!
- for a programming (and a specification) language, not for a given model of a given program:

 $\forall P \in \mathbb{P}: \forall S \in \mathbb{S}: \mathfrak{S}_{\mathbb{P}}[\![P]\!] \subset \mathcal{S}_{\mathbb{S}}[\![P,S]\!]$?

or, more simply for an *implicit specification* $\mathfrak{S}[[P]]$:

 $\forall P \in \mathbb{P} : \mathfrak{S}_{\mathbb{P}}[P] \subset \mathfrak{S}[P]$?

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Soundness and Completeness

- Soundness: for all $P \in \mathbb{P}$, if the answer is yes (no) then $\mathfrak{S}_{\mathbb{P}}[P] \subset \mathfrak{S}[P]$ (resp. $\mathfrak{S}_{\mathbb{P}}[P] \nsubseteq \mathfrak{S}[P]$)
- Completeness: for all $P \in \mathbb{P}$, if $\mathfrak{S}_{\mathbb{P}}[P] \subseteq \mathfrak{S}[P]$ ($\mathfrak{S}_{\mathbb{P}}[P] \nsubseteq$ $\mathfrak{S}[[P]]$) then the answer is yes (resp. no)

We always require SOUNDNESS!

Undecidability \implies no completeness

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Avantages of Static Analysis

- Formal specifications are implicit (no need for explicit, user-provided specifications)
- Formal semantics are approximated by the static analyzer (no user-provided models of the program)
- Formal proofs are automatic (no required user-interaction)
- Costs are low (no modification of the software production methodology)
- Scales up to 100.000 to 1.000.000 LOCS
- Rapid and large diffusion in embedded software production industries

Problems with Formal Methods

- Formal specifications (abstract machines, temporal logic, . . .) are costly, complex, error-prone, difficult to maintain, not mastered by casual programmers
- Formal semantics of the specification and programming language are inexistant, informal, irrealistic or complex
- Formal proofs are partial (static analysis), do not scale up (model checking) or need human assistance (theorem proving & proof assistants)
	- \Rightarrow High costs (for specification, proof assistance, etc).

Disadvantages of Static Analysis

- Imprecision (acceptable in some applications like WCET or program optimization)
- Incomplete for program verification
- False alarms are due to unsuccessful automatic proofs in 5 to 15% of the cases

For example, 1% of 500.000 potential (true or false) alarms is 5.000, too much to be handled by hand!

Remedies to False Alarms in ASTREE

- Astrée is specialized to specific program properties ³
- Astrée is specialized to real-time synchronous control/command programs written in C
- $-$ ASTRÉE offers possibilities of refinement 4

The cost of adapting ASTREE to a specific program, should be a small fraction of the cost to test the specific program properties verified by ASTRÉE.

proof of absence of runtime errors 4 parametrizations and analysis directives

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Abstract Interpretation

There are two fundamental concepts in computer science (and in sciences in general) :

- **Abstraction** : to reason on complex systems
- **Approximation** : to make effective undecidable computations

These concepts are formalized by abstract interpretation [CC77, Cou78, CC79, Cou81, CC92a]

[POPL TER THE STEEL INTERT INTERT INTERTATION: a unified lattice model for static analysis of programs by construction or approximation of fixpoints. In 4th ACM POPL.

[Thesis '78] P. Cousot. Méthodes itératives de construction et d'approximation de points fixes d'opérateurs monotones sur un treillis, analyse sémantique de programmes. Thèse ès sci. math. Grenoble, march 1978.

[POPL '79] P. Cousot & R. Cousot. Systematic design of program analysis frameworks. In 6^{th} ACM POPL

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Applications of Abstract Interpretation

- Static Program Analysis [CC77], [CH78], [CC79] including Dataflow Analysis; [CC79], [CC00], Set-based Analysis [CC95], Predicate Abstraction [Cou03], ...
- Grammar Analysis and Parsing [CC03];
- Hierarchies of Semantics and Proof Methods [CC92b], [Cou02];
- Typing & Type Inference [Cou97];
- (Abstract) Model Checking [CC00];
- Program Transformation (including program optimization, partial evaluation, etc) $[CC02]$;
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References

Intervals with Partitionning

Trace Partitioning

Principle:

– Semantic equivalence:

```
if (B) { C1 } else { C2 }; C3
```

```
\downarrowif (B) { C1; C3 } else { C2; C3 };
```
– More precise in the abstract: concrete execution paths are merged later.

State-based versus Trace-based Partitioning State-based partitionning at control points:

Trace-based partitionning at control points:

Delaying abstract unions in tests and loops is more precise for non-distributive abstract domains (and much less expensive than disjunctive completion).

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Case analysis with loop unrolling – Code Sample:

% astree –exec-fn main –no-trace –no-relational trace-partitioning.c |& egrep "(WARN)|(r in)" direct = \times float-interval: r in [-20, 20] >

%

% astree -exec-fn main -no-partition -no-trace -no-relational trace-partitioning.c \ |& egrep "(WARN)|(r in)"

direct = \langle float-interval: r in [-100, 100] > %

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- $-$ with (cont'd) **NEW**
	- union [Min06a]
	- pointer arithmetics & casts [Min06a]
- without
	- dynamic memory allocation
	- recursive function calls
	- unstructured/backward branching
	- conflicting side effects
	- C libraries, system calls (parallelism)

Such limitations are quite common for embedded safety-critical software.

Concrete Operational Semantics

- International norm of C (ISO/IEC 9899:1999)
- restricted by implementation-specific behaviors depending upon the machine and compiler (e.g. representation and size of integers, IEEE 754-1985 norm for floats and doubles)
- restricted by user-defined programming guidelines (such as no modular arithmetic for signed integers, even though this might be the hardware choice)
- restricted by program specific user requirements (e.g. assert, execution stops on first runtime error⁹)

 $\frac{9}{9}$ semantics of C unclear after an error, equivalent if no alarm

The Class of Considered Periodic Synchronous Programs

declare volatile input, state and output variables; initialize state and output variables; **loop forever**

- read volatile input variables,
- compute output and state variables,
- write to output variables;
	- **__ASTREE_wait_for_clock ()**;

end loop

Task scheduling is static:

- Requirements: the only interrupts are clock ticks;
- Execution time of loop body less than a clock tick, as verified by the aiT WCET Analyzers $[FHL^+01]$.

 $\begin{array}{c} \hline \end{array}$ Seminar, SUNY SB CS, 18/01/2008 $-46 -$ © P. Cousot $\mathcal{O}_{\mathbf{2}}\mathcal{C}$

The Semantics of C is Hard (Ex. 1: Floats)

"Put x in $[m, M]$ modulo $(M - m)$ ":

 $x' = x - (int) ((x-m)/(M-m)) * (M-m)$;

- The programmer thinks $x' \in [m, M]$
- But with $M = 4095$, $m = -M$, IEEE double precision, and x is the greatest float strictly less than M, then $x' = m - \epsilon$ (ϵ very small).

Floats are not real.

Static analysis with A STRÉE¹⁶ % cat -n double-error.c 2 int main $() \{$ 3 double x; float y, z, r;; $4 / * x = 1$ dexp(1.,50)+1dexp(1.,26); */ 5 x = 1125899973951488.0; 6 $y = x + 1$: $7 \times = x - 1$; $8 \t r = v - z;$ 9 __ASTREE_log_vars((r)); 10 } % gcc double-error.c $\%$ /a. out. 134217728.000000 % astree -exec-fn main -print-float-digits 10 double-error.c $\&$ grep "r in direct = <float-interval: r in [-134217728, 134217728] > $\overline{16}$ Astriangleright a worst-case assumption on the rounding $(+\infty, -\infty, 0,$ nearest) hence the possibility to get -134217728. Seminar, SUNY SB CS, 18/01/2008 $-67 -$ © P. Cousot \bigotimes M

Static analysis with ASTRÉE

```
% cat -n rounding.c
     1 int main () {
     2 double x; x = 0.0;
     3 while (1) {
     4 \times = \times + 1.0/10.0;
     5 \text{I-MSTREE}\_\text{log}\_\text{vars}((x));6 ASTERE wait for clock(());
     7 }
     8 }
% cat rounding.config
 __ASTREE_max_clock((1000000000));
% astree -exec-fn main -config-sem rounding.config -unroll 0 rounding.c\cdot|& egrep "(x in)|(\1x)|(\text{WARN})" | tail -2
direct = \timesfloat-interval: x in [0.1, 200000040.938] >
  |x| \le 1.*((0. + 0.1/(1.-1))*(1.)^{\circ} clock - 0.1/(1.-1)) + 0.1\leq 200000040 938
     Seminar, SUNY SB CS, 18/01/2008 -69 - © P. Cousot \frac{1}{100} INRIA
```
Example of accumulation of small rounding errors

The Patriot missile failure

– "On February $25th$, 1991, a Patriot missile . . . failed to track and intercept an incoming Scud (*)."

- The software failure was due to accumulated rounding error (f)
- $(*)$ This Scud subsequently hit an Army barracks, killing 28 Americans.
- (1) "Time is kept continuously by the system's internal clock in tenths of seconds"
- "The system had been in operation for over 100 consecutive hours"
- "Because the system had been on so long, the resulting inaccuracy in the time calculation caused the range gate to shift so much that the system could not track the incoming Scud"

Example 3: Time Dependent Deviations [Fer05]

```
% cat retro.c
typedef enum {FALSE=0, TRUE=1} BOOL;
BOOL FIRST;
volatile BOOL SWITCH;
volatile float E;
float P, X, A, B;
void dev( )
f \times = E;
  if (FIRST) { P = X; }
  else
    {P = (P - ((( (2.0 * P) - A) - B))* 4.491048e-03)); };
  B = A:
  if (SWITCH) {A = P:}else {A = X:}}
                                          void main()
                                          f FIRST = TRUE;
                                            while (TRUE) {
                                              dev():
                                              FIRST = FAISE__ASTREE_wait_for_clock(());
                                            }}
                                          % cat retro.config
                                          _ASTREE_volatile_input((E [-15.0, 15.0]));
                                          __ASTREE_volatile_input((SWITCH [0,1]));
                                          __ASTREE_max_clock((3600000));
                                          |P| <= (15. + 5.87747175411e-39
                                          / 1.19209290217e-07) * (1
                                          + 1.19209290217e-07)ˆclock
                                          - 5.87747175411e-39 /
                                          1.19209290217e-07 <= 23.0393526881
      Seminar, SUNY SB CS, 18/01/2008 - 83 - © P. Cousot \mathcal{C}_{\mathcal{B}} Incompleteness
                                                                                                        Astrée does not know that
                                                                                                                                \forall x, y \in \mathbb{Z} : 7y^2 - 1 \neq x^2so on the following program
                                                                                                        void main() { int x, y;
                                                                                                           if ((-4681 \times y) \&x (y \times 4681) \&x (x \times 32767) \&x (-32767 \times x) \&x ((7*y*y-1) == x*x))\{ y = 1 / x; \};
                                                                                                        }
                                                                                                        it produces a false alarm
                                                                                                        % astree –exec-fn main false-alarm.c |& egrep "WARN"
                                                                                                         false-alarm.c:5.9-14::[call#main@1:]: WARN: integer division by zero ([-32766, 32766]
                                                                                                         and \{1\} / Z)
                                                                                                         %
                                                                                                              Seminar, SUNY SB CS, 18/01/2008 -84 - © P. Cousot \mathbb{C} \mathbb{C} \mathbb{R} \mathbb{R} IN RIA
                     Zero False Alarm Objective
Industrial constraints require ASTRÉE to be extremely
precise:
– Astrée is designed for a well-identified family of pro-
   grams
– The analysis can be tuned using
   - parameters
   - analysis directives (which insertion can be automated)
   - extensions of the analyzer (by the tool designers)
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                                                                                                                            Example of directive
                                                                                                        % cat repeat1.c
                                                                                                         typedef enum {FALSE=0,TRUE=1} BOOL;
                                                                                                        int main () {
                                                                                                           int x = 100; BOOL b = TRUE;
                                                                                                           while (b) {
                                                                                                             x = x - 1;
                                                                                                             b = (x > 0);
                                                                                                           }
                                                                                                         }
                                                                                                        % astree –exec-fn main repeat1.c |& egrep "WARN"
                                                                                                         repeat1.c:5.8-13::[call#main@2:loop@4>=4:]: WARN: signed int arithmetic
                                                                                                         range [-2147483649, 2147483646] not included in [-2147483648, 2147483647]
                                                                                                        %
                                                                                                              Seminar, SUNY SB CS, 18/01/2008 -86 - C P. Cousot \mathbb{Z}_2^l \mathbb{Z}^l in RIA
```
Example of directive (Cont'd) % cat repeat2.c typedef enum {FALSE=0,TRUE=1} BOOL; 4. The industrial use of ASTRÉE int main () { int $x = 100$; BOOL $b = TRUE$; $ASTEREE_{boolean_pack}((b,x));$ while (b) { $x = x - 1$; $b = (x > 0);$ } } % astree –exec-fn main repeat2.c |& egrep "WARN" % References The insertion of this directive could have been automated in D. Delmas and J. Souyris. AsTRÉE: from Research to Industry. Proc. 14th Int. Symp. SAS '07, G. Filé and H. Riis-Nielson (eds), 22–24 Aug. 2007, Kongens Lyngby, DK, LNCS 4634, pp. 437–451, Springer. Astrée. Seminar, SUNY SB CS, 18/01/2008 $-87 -$ © P. Cousot $\mathcal{O}_{\mathcal{A}}$ Seminar, SUNY SB CS, 18/01/2008 $-$ 88 $-$ (c) P. Cousot $\bigotimes_{i=1}^{n}$ \mathbb{Z}_{INRIA} Example application Digital Fly-by-Wire Avionics¹⁹ – Primary flight control software of the Airbus A340 family/A380 fly-by-wire system – C program, automatically generated from a proprietary high-level specification (à la Simulink/SCADE) – A340 family: 132,000 lines, 75,000 LOCs after prepro-

cessing, 10,000 global variables, over 21,000 after ex-

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pansion of small arrays, now \times 2

 $-. A380: \times 3/7$

 19 The electrical flight control system is placed between the pilot's controls (sidesticks, rudder pedals) and the control surfaces of the aircraft, whose movement they control and monitor.

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Benchmarks (Airbus A340 Primary Flight Control Software)

- V1 20 , 132,000 lines, 75,000 LOCs after preprocessing
- Comparative results (commercial software):
	- 4,200 (false?) alarms, 3.5 days;
- Our results:
	- **0** alarms, 40mn on 2.8 GHz PC, 300 Megabytes \longrightarrow A world première in Nov. 2003!

20 "Flight Control and Guidance Unit" (FCGU) running on the "Flight Control Primary Computers" (FCPC). The three primary computers (FCPC) and two secondary computers (FCSC) which form the A340 and A330 electrical flight control system are placed between the pilot's controls (sidesticks, rudder pedals) and the control surfaces of the aircraft, whose movement they control and monitor.

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(Airbus A380 Primary Flight Control Software)

- **0** alarms (Nov. 2004), after some additional parametrization and simple abstract domains developments
- Now at 1,000,000 lines!

34h,

- 8 Gigabyte
- \longrightarrow A world grand première!

The main loop invariant for the A340 V1

- A textual file over 4.5 Mb with
- 6,900 boolean interval assertions ($x \in [0; 1]$)
- 9,600 interval assertions $(x \in [a; b])$
- 25,400 clock assertions $(x + c$ lk $\in [a; b] \wedge x c$ lk $\in [a; b]$)
- 19,100 additive octagonal assertions $(a \leq x + y \leq b)$
- 19,200 subtractive octagonal assertions $(a \le x-y \le b)$
- 100 decision trees
- 60 ellipse invariants, etc . . .

involving over 16,000 floating point constants (only 550 appearing in the program text) \times 75,000 LOCs.

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Possible origins of imprecision and how to fix it

In case of false alarm, the imprecision can come from:

- Abstract transformers (not best possible) \longrightarrow improve algorithm;
- Automatized parametrization (e.g. variable packing) \longrightarrow improve pattern-matched program schemata;
- Iteration strategy for fixpoints \longrightarrow fix widening ²¹;
- Inexpressivity i.e. indispensable local inductive invariant are inexpressible in the abstract \longrightarrow add a new abstract domain to the reduced product (e.g. filters).

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 21 This can be very hard since at the limit only a precise infinite iteration might be able to compute the proper abstract invariant. In that case, it might be better to design a more refined abstract domain

Characteristics of the Astree Analyzer (Cont'd)

- **Static:** compile time analysis (\neq run time analysis Rational Purify, Parasoft Insure++)
- **Program Analyzer:** analyzes programs not micromodels of programs (\neq PROMELA in SPIN or Alloy in the Alloy Analyzer)
- **Automatic:** no end-user intervention needed (\neq ESC Java, ESC Java 2), or PREfast (annotate functions with intended use)

Characteristics of the ASTRÉE Analyzer

- Sound: $-$ Astree is a bug eradicator: finds all bugs in a well-defined class (runtime errors)
- $-$ Astream is not a bug hunter: finding some bugs in a well-defined class (e.g. by bug pattern detection like FindBugs[™], PREfast or PMD)
- ASTRÉE is exhaustive: covers the whole state space (\neq MAGIC, CBMC)
- Astrée is comprehensive: never omits potential errors (\neq UNO, CMC from coverity.com) or sort most probable ones to avoid overwhelming messages (\neq Splint)

Characteristics of the Astree Analyzer (Cont'd)

- **Multiabstraction:** uses many numerical/symbolic abstract domains (\neq symbolic constraints in Bane or the canonical abstraction of TVLA)
- **Infinitary:** all abstractions use infinite abstract domains with widening/narrowing $(\neq$ model checking based analyzers such as Bandera, Bogor, Java PathFinder, Spin, VeriSoft)
- **Efficient:** always terminate (\neq counterexample-driven automatic abstraction refinement BLAST, SLAM)

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Characteristics of the Astree Analyzer (Cont'd)

- **Extensible/Specializable:** can easily incorporate new abstractions (and reduction with already existing abstract domains) (\neq general-purpose analyzers PolySpace Verifier)
- **Domain-Aware:** knows about control/command (e.g. digital filters) (as opposed to specialization to a mere programming style in C Global Surveyor)
- Parametric: the precision/cost can be tailored to user needs by options and directives in the code

The Future of the ASTRÉE Analyzer

- Astrée has shown usable and useful in one industrial context (electric flight control):
	- as a R & D tool for A340 V2 and A380,
	- as a production tool for the A350;
- More applications are forthcoming (ES_PASS project);
- Industrialization is simultaneously under consideration.

Characteristics of the Astree Analyzer (Cont'd)

- **Automatic Parametrization:** the generation of parametric directives in the code can be programmed (to be specialized for a specific application domain)
- **Modular:** an analyzer instance is built by selection of O-CAML modules from a collection each implementing an abstract domain
- **Precise:** very few or no false alarm when adapted to an application domain \longrightarrow it is a VERIFIER!

The Future of Abstract Interpretation

- Abstract interpretation is
	- a theory
	- with effective applications
	- and unprecedented industrial accomplishments.
- Further investigations of the theory are needed (while its scope of application broaden)

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– The demand for applications is quasi-illimited

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