

The analytic semantics of weakly consistent parallelism

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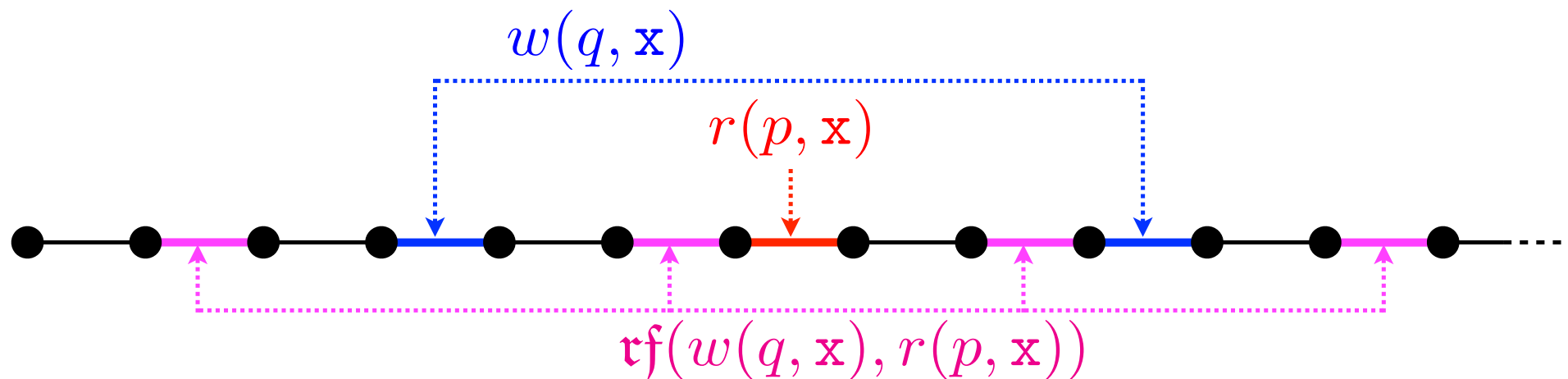
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Analytic semantics

Weak consistency models (WCM)

- **Sequential consistency:**
reads $\text{read}(p, x)$ are implicitly *coordinated* with writes $w(q, x)$
- **WCM:**
No implicit coordination (depends on architecture, program dependencies, and explicit fences)



Analytic semantic specification

- **Anarchic semantics:**
describes computations, no constraints on communications
- **cat specification (Jade Alglave & Luc Maranget):**
imposes architecture-dependent communication constraints
- **Hierarchy of anarchic semantics:**
many different styles to describe the same computations (e.g. interleaved versus true parallelism)

Example: load buffer (LB)

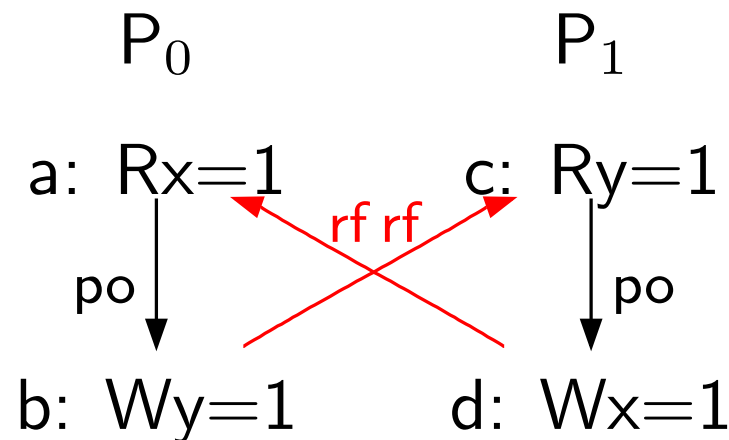
- Program:

{ x = 0; y = 0; }			
P0		P1	;
r[] r1 x		r[] r2 y	;
w[] y 1		w[] x 1	;
exists(0:r1=1 /\ 1:r2=1)			

- Example of execution trace $t \in S^\perp \llbracket P \rrbracket$:

$t = w(\text{start}, x, 0) w(\text{start}, y, 0) r(\text{P0}, x, 1) \text{rf}[w(\text{P1}, x, 1), r(\text{P0}, x, 1)] w(\text{P0}, y, 1) r(\text{P1}, y, 1)$
 $w(\text{P1}, x, 1) \text{rf}[w(\text{P0}, y, 1), r(\text{P1}, y, 1)] r(\text{finish}, x) \text{rf}[w(\text{P1}, x, 1), r(\text{finish}, x, 1)]$
 $r(\text{finish}, y, 1) \text{rf}[w(\text{P0}, y, 1), r(\text{finish}, y, 1)]$

- Abstraction to cat candidate execution $\alpha_{\Xi}(t)$



Example: load buffer (LB), cont'd

- cat specification:

`acyclic (po | rf)+`

The cat semantics rejects this execution $\alpha_{\Xi}(t)$:

$$\text{cat} \llbracket \text{cat} \rrbracket (\alpha_{\Xi}(t)) = \text{false}$$

The WCM semantics

- Abstraction to a **candidate execution**:

$$\alpha_{\Xi}(t) \triangleq \langle \alpha_e(t), \alpha_{po}(t), \alpha_{rf}(t), \alpha_{iw}(t), \alpha_{fw}(t) \rangle$$

$$\alpha_{\Xi}(\mathcal{S}) \triangleq \{ \langle t, \alpha_{\Xi}(t) \rangle \mid t \in \mathcal{S} \}$$

- The **cat semantics**:

$$\alpha_{\text{cat}}(\mathcal{S}) \triangleq \{ t \mid \langle t, \Xi \rangle \in \mathcal{S} \wedge \text{cat}(\Xi) \}$$

- The **WCM semantics**:

$$\alpha_{\text{cat}} \circ \alpha_{\Xi}(\mathcal{S}[\mathcal{P}])$$

$$\text{GC: } \langle \wp(\mathcal{E}^{+\infty}), \sqsubseteq \rangle \xrightleftharpoons[\alpha_{\Xi}]{\gamma_{\Xi}} \langle \wp(\mathcal{E}^{+\infty} \times \Xi), \sqsubseteq \rangle \xrightleftharpoons[\alpha_{\text{cat}}]{\gamma_{\text{cat}}} \langle \wp(\mathcal{E}^{+\infty}), \sqsubseteq \rangle$$

Definition of the anarchic semantics

Axiomatic parameterized definition of the anarchic semantics

- The semantics $S^\perp \llbracket P \rrbracket$ is a finite/infinite **sequence of interleaved events of processes** satisfying well-formedness conditions.
- Example: **computation** (local variable assignment)

register assignment event
by process p in trace τ

unique event stamp θ

$$\forall p \in \mathbb{P} \dot{i} . \forall k \in]1, 1 + |\tau|[. \forall \ell \in \mathbb{L}(p) . \forall v \in \mathcal{D} . \quad (\text{Wf}_{22}(\tau))$$

$$(\exists \theta \in \mathfrak{P}(p) . \bar{\tau}_k = \mathfrak{a}(\langle p, \ell, \mathbf{r} := e, \theta \rangle, v))$$

$$\implies (\ell \in N^p(\tau, k) \wedge \text{action}(p, \ell) = \mathbf{r} := e \wedge v = E^p \llbracket e \rrbracket (\tau, k - 1)) .$$

control of process
 p is at label ℓ

action of process p
is at label ℓ is a
register assignment

value v of e is
evaluated by past-
travel

Axiomatic parameterized definition of the anarchic semantics

- Example: communication

- a read event is initiated by a read action:

*read event by
process p in trace τ*

*unique media
variable (L-value)*

$$\forall p \in \mathbb{P}^i . \forall k \in]1, 1 + |\tau|[. \forall \ell \in \mathbb{L}(p) . \quad (\text{Wf}_{23}(\tau))$$

$$(\exists \theta \in \mathfrak{P}(p) . (\bar{\tau}_k = \mathbf{r}(\langle p, \ell, \mathbf{r} := \mathbf{x}, \theta \rangle, \mathbf{x}_\theta)))$$

$$\implies (\ell \in N^p(\tau, k) \wedge \text{action}(p, \ell) = \mathbf{r} := \mathbf{x}) .$$

- a read must read-from (**rf**) a write (fairness):

$$\forall p \in \mathbb{P}^i . \forall i \in]1, 1 + |\tau|[. \forall r \in \mathfrak{Rf}(p) . \quad (\text{Wf}_{26}(\tau))$$

$$(\bar{\tau}_i = r) \implies (\exists j \in]1, 1 + |\tau|[. \exists w \in \mathfrak{W}^i . \bar{\tau}_j = \mathbf{rf}[w, r]) .$$

Axiomatic parameterized definition of the anarchic semantics

- Predictive evaluation of media variables:

$$V_{(32)}^p[[\mathbf{x}_\theta]](\tau, k) \triangleq v \text{ where } \exists! i \in [1, 1 + |\tau|[\cdot (\bar{\tau}_i = \mathbf{r}(\langle p, \ell, \mathbf{r} := \mathbf{x}, \theta \rangle, \mathbf{x}_\theta)) \wedge \\ \exists! j \in [1, 1 + |\tau|[\cdot (\bar{\tau}_j = \mathbf{rf}[\mathbf{w}(\langle p', \ell', \mathbf{x} := e', \theta' \rangle, v), \bar{\tau}_i])$$

- Local path-based evaluation of an expression:

$$E_{(30)}^p[[\mathbf{r}]](\tau, k) \triangleq v \quad \text{if } k > 1 \wedge ((\bar{\tau}_k = \mathbf{a}(\langle p, \ell, \mathbf{r} := e, \theta \rangle, v)) \vee \\ (\bar{\tau}_k = \mathbf{r}(\langle p, \ell, \mathbf{r} := \mathbf{x}, \theta \rangle, \mathbf{x}_\theta) \wedge V^p[[\mathbf{x}_\theta]](\tau, k) = v))$$

$$E_{(30)}^p[[\mathbf{r}]](\tau, 1) \triangleq I[[0]] \quad \text{i.e. } \bar{\tau}_1 = \epsilon_{\text{start}} \text{ by } \mathbf{Wf}_{15}(\tau)$$

$$E_{(30)}^p[[\mathbf{r}]](\tau, k) \triangleq E_{(30)}^p[[\mathbf{r}]](\tau, k - 1) \quad \text{otherwise.}$$

Abstractions of the anarchic semantics

Abstractions

- **Semantics:**

$$S^\perp \llbracket P \rrbracket \triangleq \lambda \langle \mathcal{B}, \text{sat}, \mathcal{D}, I, \mathcal{G}, V, E, N \rangle \cdot \{ \tau \in \mathcal{T} \llbracket P \rrbracket \mid_{\cong} \mid \text{Wf}_{15}(\tau) \wedge \dots \wedge \text{Wf}_{29}(\tau) \}$$

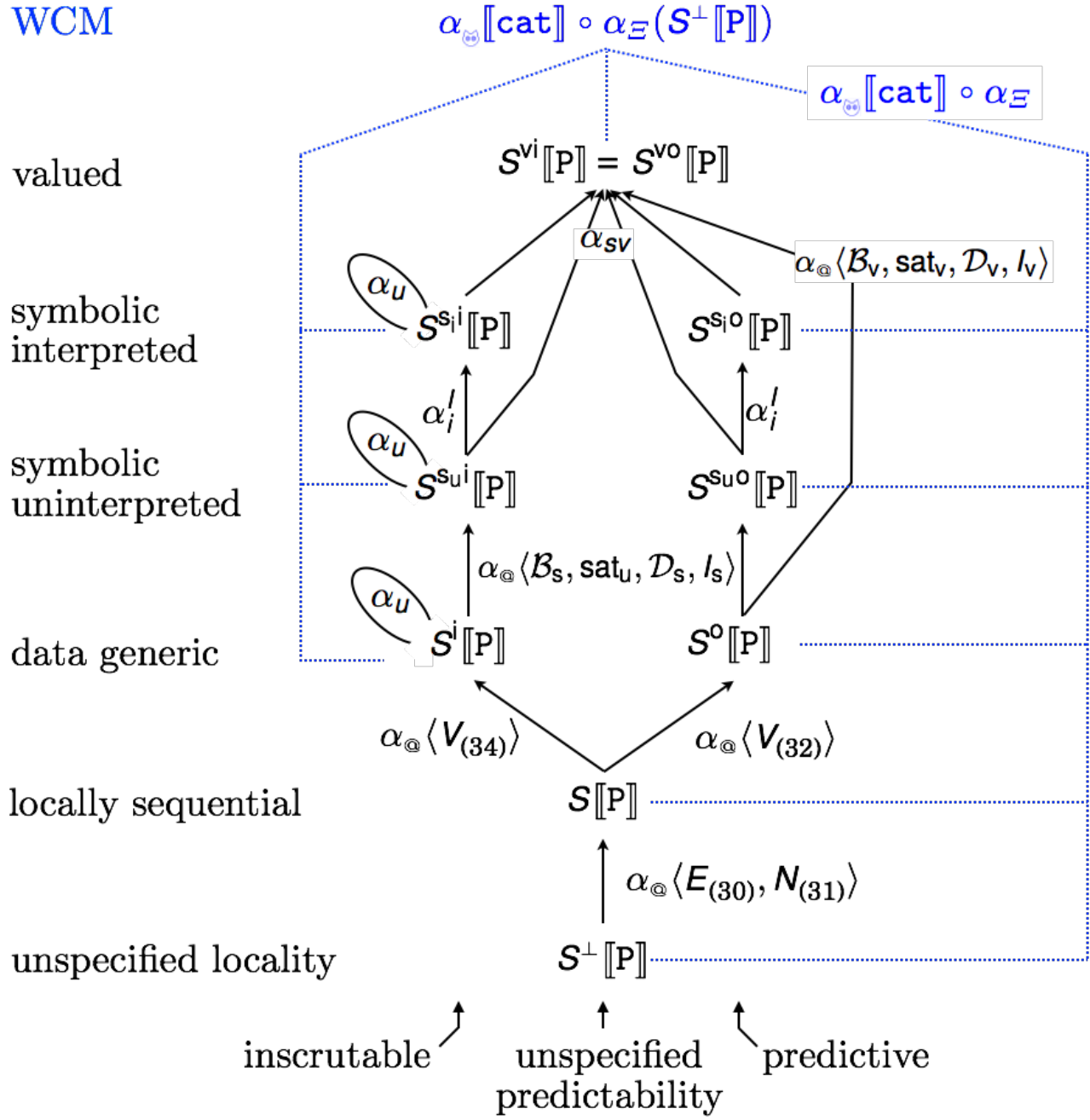
↑
↑
parameters of the semantics

↑
↑
trace well-formedness conditions

- **Examples of abstractions:**

- Choose data (e.g. ground values, uninterpreted symbolic expressions, interpreted symbolic expressions i.e. “symbolic guess”)
- Bind parameters (e.g. how expressions are evaluated)
- ...

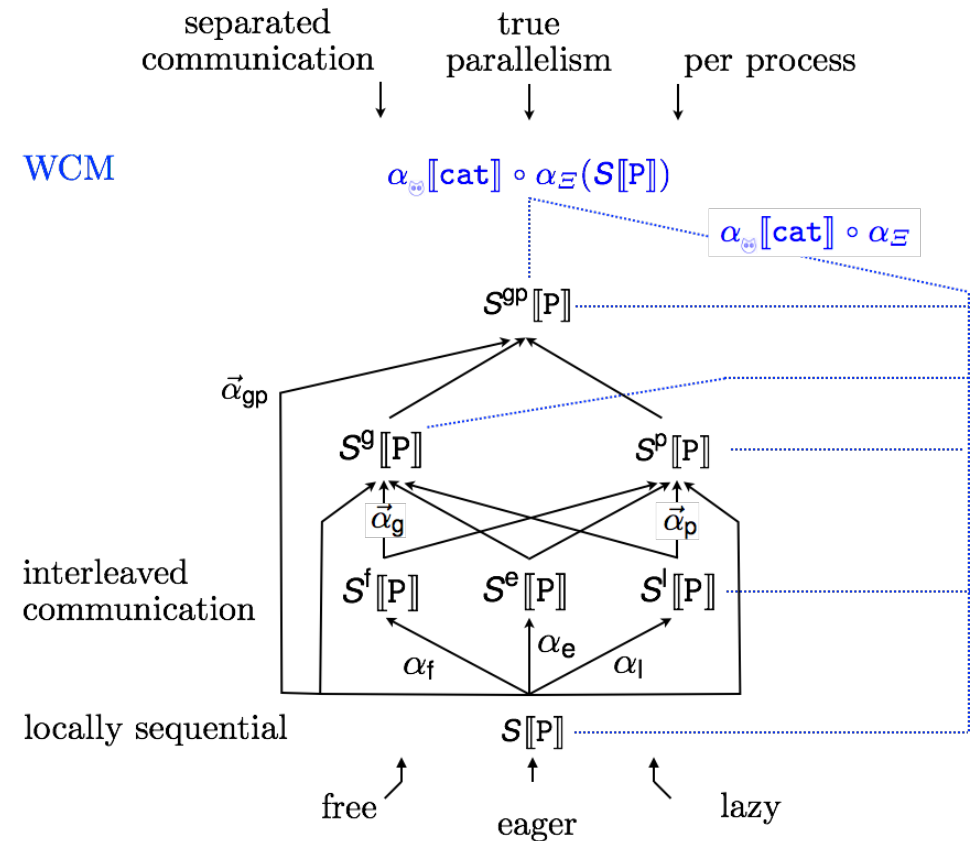
The hierarchy of interleaved semantics



True parallelism

- Extract from interleaved executions:
 - The **subtrace of each process** (sequential execution)
 - The **rf communication relation** (interactions between processes)

⇒ no more global time ! WCM



States

- At each point in a trace, **the state abstracts the past computation history** up to that point
- Example: classical **environment** (assigning values to register at each point k of the trace):

$$\rho^p(\tau, k) \triangleq \lambda \mathbf{r} \in \mathbb{R}(p) \cdot E^p[\mathbf{r}](\tau, k)$$

$$\nu^p(\tau, k) \triangleq \lambda \mathbf{x}_\theta \cdot V_{(32)}^p[\mathbf{x}_\theta](\tau, k)$$

Prefixes, transitions, ...

- Abstract traces by their prefixes:

$$\overleftarrow{\alpha}(S) \triangleq \bigcup \{ \overleftarrow{\alpha}(\tau) \mid \tau \in S \}$$

$$\overleftarrow{\alpha}(\tau) \triangleq \{ \tau \langle j \rangle \mid j \in [1, 1 + |\tau|] \}$$

$$\tau \langle j \rangle \triangleq \langle \xrightarrow{\overline{\tau}_i} \underline{\tau}_i \mid i \in [1, 1 + j[\rangle$$

- and transitions: extract transitions from traces

⇒ communication fairness is lost, inexact abstraction,

⇒ add fairness condition

Effect of the cat specification on the hierarchy

The cat abstraction

- The same cat specification $\alpha_{\text{cat}} \llbracket \text{cat} \rrbracket$ applies equally to any concurrent execution abstraction α_{Ξ} of any interleaved/truly parallel semantics in the hierarchy
- The appropriate level of abstraction to specify WCM:
 - No states, only marker (e.g. fence), r, w, rf(w,r) events
 - No values in events
 - No global time (only po order of events per process)
 - Time of communications forgotten (only rf of who communicates with whom)

Conclusion

Conclusion

- The hierarchy of **anarchic semantics** describe the same computations and potential communications in very different styles
- The **cat semantics** restricts communications to a machine/network architecture in the same way for all semantics in the hierarchy
- This idea of **parameterized semantics at various levels of abstraction** is useful for
 - **Verification**
 - **Static analysis**

The End