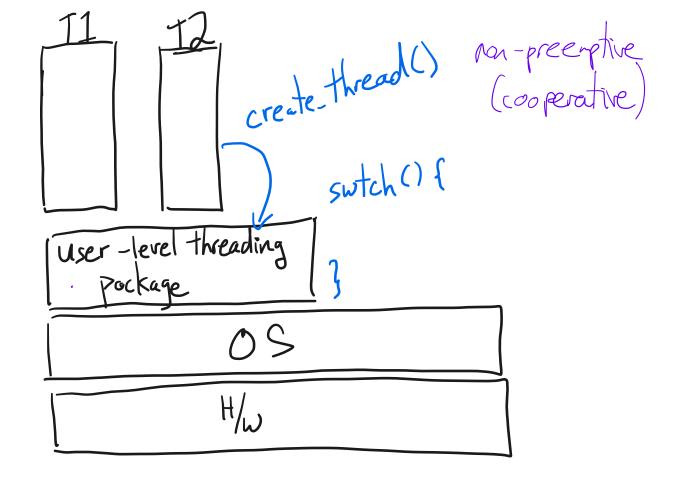
1 | Last time 12. Context switches (WeensyOS) 13. User-level threading, intro ny. Context switches (user-level threading) swtch() yield () Ooperative multithreading preemp 1 1 6. Preemptive user-level multithreading reap 1 7 1 7. mmap () again

2. Context switches in Weensy OS (see pictures at the end)

3. User-level threading

preenptile



4. Context switches (user space) T3 stack - switch registers active Eswitch page tables TI stack TZ stack

text

user-level threading package

I non-blocking

mmap

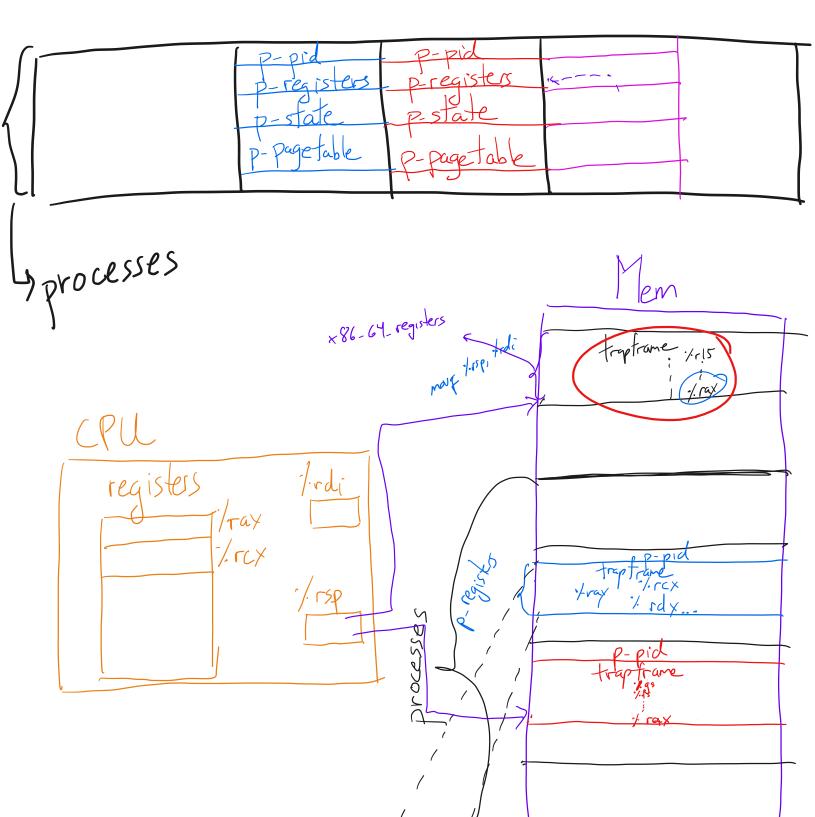
Mmap

VA of "file" > phys addr in RAM (in buffer cache) kernel
buter cache (RAM)

Caching

(

Context switches in Weensy OS



traptrame

/-ss

-/rsp

-/rtlags

-/-cs

-/-rip

No	v 10, 24		Page 1/2
1 2		Fall 2024 12 (Class 17)	مام
3 4		r-level threads and swtch()	٩
5 6	We'	ll study this in the context of user-level threads	
8	Per	r-thread state in thread control block:	
9 10		typedef struct tcb {	' '
11 12		unsigned long saved_rsp; /* Stack pointer of thread */ char *t_stack; /* Bottom of thread's stack *,	/
13 14		/* ··· */ };	igherause
15 16	Mac	chine-dependent thread initialization function:	because
17	riac	void thread_init(tcb **t, void (*fn) (void *), void *arg);	k grow
18 19			down.
20 21	Mac	Chine-dependent thread-switch function:	
22 23		void swtch(tcb *cuffent, tcb text);	
24 25	Imp	plementation of swtch(current, next):	1
26		# gcc x86-64 calling convention:	(
27 28		<pre># on entering swtch(): # register %rdi holds first argument to the function ("current "current")</pre>	
29 30		<pre># register %rsi holds second argument to the function ("next"</pre>	")
31		# Save call-preserved (aka "callee-saved") regs of 'current'	
32 33		pushq %rbp pushq %rbx	
34		pushq %r12	
35 36		pushq %r13 pushq %r14	V
37		pushq %r15	}
38 39		# store old stack pointer, for when we swtch() back to "curren	nt" later
40		movq %rsp, (%rdi) # %rdi->saved_rsp = %s	rsp
41 42		movq (%rsi), %rsp # %rsp = %rsi->saved_1	rsp
43		# Restore call-preserved (aka "callee-saved") regs of 'next'	
44 45		popq %r15 popq %r14	14
46		popq %r13 -	
47 48		popq %r12 _ popq %rbx _	
49		popq %rbp	
50 51		# Resume execution, from where "next" was when it last entered	d swtch()
52		ret	, , , , , , , , , , , , , , , , , , ,
53 54		_	
		Acin III	
	-0	t-advisor /·rbp	uithia
	(0	cot all within yeld > ret ad	dr yield()
		-> 1/rbp	′
		1 1 / cho	
		1/rbx	
		i o c	r
	,	1/10 //r15 Oxff100 / // T /r15	
	1.158		
(2100 F		
par	** 		
<u></u>	arr Niarra	ember 10, 2024 /	swto

```
swtch.txt
Nov 10, 24 10:48
                                                                                 Page 2/2
  2. Example use of swtch(): the yield() call.
56
       A thread is going about its business and decides that it's executed for long enough. So it calls yield(). Conceptually, the overall system needs
58
        to now choose another thread, and run it:
        void yield() { blue
                          = pick_next_thread(); /* get a runnable thread */
            tcb* current = get_current_thread();
         swtch(current, next);
            /* when 'current' is later rescheduled, it starts from here */
69
70
   3. How do context switches interact with I/O calls?
72
73
        This assumes a user-level threading package.
        The thread calls something like "fake_blocking_read()". This looks
        to the _thread_ as though the call blocks, but in reality, the call
        is not blocking:
        int fake_blocking_read(int fd, char* buf, int num) {
80
81
            int pread = -1;
82
            while (nread == -1) {
                 /* this is a non-blocking read() syscall */
                nread = read(fd, buf, num);
                if (nread == -1 && errno == EAGAIN) {
89
                      ^{\star} read would block. so let another thread run \,
91
92
                      * and try again later (next time through the
                      * loop).
93
                     yield();
95
96
97
98
99
            return nread;
100
101
102
103
104
£500
```

Sunday November 10, 2024

swtch.txt

1/1

TCB.

these stacks are
these stacks are
inside the single
inside space panying
address accompanying
diagram accompanying
withes notes.

Context whese
earlier in

0xf...floo = 0xf...f500 0xf...f500 0xf...f600red blue