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```
Implementation of spinlocks and mutexes
   1. Here is a BROKEN spinlock implementation:
            struct Spinlock {
             int locked;
           void acquire(Spinlock *lock) {
9
10
                if (lock->locked == 0) { // A}
11
12
                 lock->locked = 1;
13
                 break:
15
16
17
            void release (Spinlock *lock) {
18
19
             lock \rightarrow locked = 0;
20
21
            What's the problem? Two acquire()s on the same lock on different
22
23
            CPUs might both execute line A, and then both execute B. Then
            both will think they have acquired the lock. Both will proceed.
24
25
            That doesn't provide mutual exclusion.
```

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  2. Correct spinlock implementation
        Relies on atomic hardware instruction. For example, on the x86-64,
29
30
                    "xchq addr, %rax"
31
           does the following:
32
33
            (i) freeze all CPUs' memory activity for address addr
34
            (ii) temp <-- *addr
35
            (iii) *addr <-- %rax
36
37
            (iv) %rax <-- temp
            (v) un-freeze memory activity
38
39
40
        /* pseudocode */
41
        int xchg_val(addr, value) {
            %rax = value;
42
            xchg (*addr), %rax
43
44
45
46
        /* bare-bones version of acquire */
47
        void acquire (Spinlock *lock) {
48
          pushcli();
                        /* what does this do? */
49
          while (1) {
            if (xchg_val(&lock->locked, 1) == 0)
51
             break;
52
53
54
55
        void release(Spinlock *lock) {
56
          xchq_val(&lock->locked, 0);
57
          popcli(); /* what does this do? */
58
59
60
        /* optimization in acquire; call xchq_val() less frequently */
        void acquire(Spinlock* lock) {
62
63
64
            while (xchg_val(&lock->locked, 1) == 1) {
65
                while (lock->locked);
66
67
68
        The above is called a *spinlock* because acquire() spins. The
69
        bare-bones version is called a "test-and-set (TAS) spinlock"; the
70
       other is called a "test-and-test-and-set spinlock".
71
72
73
        The spinlock above is great for some things, not so great for
74
        others. The main problem is that it *busy waits*: it spins,
75
        chewing up CPU cycles. Sometimes this is what we want (e.g., if
76
        the cost of going to sleep is greater than the cost of spinning
77
        for a few cycles waiting for another thread or process to
78
        relinquish the spinlock). But sometimes this is not at all what we
        want (e.g., if the lock would be held for a while: in those
79
80
        cases, the CPU waiting for the lock would waste cycles spinning
81
        instead of running some other thread or process).
82
83
        NOTE: the spinlocks presented here can introduce performance issues
84
        when there is a lot of contention. (This happens even if the
85
        programmer is using spinlocks correctly.) The performance issues
        result from cross-talk among CPUs (which undermines caching and
86
        generates traffic on the memory bus). If we have time later, we will
88
        study a remediation of this issue (search the Web for "MCS locks").
89
        ANOTHER NOTE: In everyday application-level programming, spinlocks
90
        will not be something you use (use mutexes instead). But you should
92
        know what these are for technical literacy, and to see where the
93
        mutual exclusion is truly enforced on modern hardware.
```

26

## Sep 25, 24 9:47 spinlock-mutex.txt Page 3/3 95 3. Mutex implementation

```
95 3. Mutex implementation
96
97 The intent of a mutex is to avoid busy waiting: if the lock is not
98 available, the locking thread is put to sleep, and tracked by a
99 queue in the mutex. The next page has an implementation.
100
101
```

```
fair-mutex.c
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   #include <sys/queue.h>
   typedef struct thread {
        // ... Entries elided.
        STAILQ_ENTRY(thread_t) qlink; // Tail queue entry.
   } thread t;
   struct Mutex {
        // Current owner, or 0 when mutex is not held.
       thread_t *owner;
11
12
        // List of threads waiting on mutex
       STAILQ(thread_t) waiters;
13
15
        // A lock protecting the internals of the mutex.
16
       Spinlock splock; // as in item 1, above
17 };
19
   void mutex_acquire(struct Mutex *m) {
20
21
       acquire(&m->splock);
22
23
        // Check if the mutex is held; if not, current thread gets mutex and returns
24
        if (m->owner == 0) {
25
            m->owner = id_of_this_thread;
           release(&m->splock);
26
27
        } else
            // Add thread to waiters.
28
            STAILQ_INSERT_TAIL(&m->waiters, id_of_this_thread, qlink);
29
30
            // Tell the scheduler to add current thread to the list
31
32
            // of blocked threads. The scheduler needs to be careful
33
            // when a corresponding sched_wakeup call is executed to
34
            // make sure that it treats running threads correctly.
35
            sched_mark_blocked(&id_of_this_thread);
37
            // Unlock spinlock.
38
            release (&m->splock);
39
            // Stop executing until woken.
41
            sched_swtch();
42
43
            // When we get to this line, we are quaranteed to hold the mutex. This
            // is because we can get here only if context-switched-TO, which itself
44
45
            // can happen only if this thread is removed from the waiting queue,
            // marked "unblocked", and set to be the owner (in mutex_release()
46
            // below). However, we might have held the mutex in lines 39-42
            // (if we were context-switched out after the spinlock release(),
48
49
            // followed by being run as a result of another thread's release of the
50
            // mutex). But if that happens, it just means that we are
            // context-switched out an "extra" time before proceeding.
52
53
  void mutex_release(struct Mutex *m) {
56
        // Acquire the spinlock in order to make changes.
57
       acquire(&m->splock);
59
        // Assert that the current thread actually owns the mutex
60
        assert(m->owner == id_of_this_thread);
61
        // Check if anyone is waiting.
       m->owner = STAILQ_GET_HEAD(&m->waiters);
63
64
        // If so, wake them up.
65
66
        if (m->owner) {
67
            sched_wakeone(&m->owner);
68
            STAILQ_REMOVE_HEAD(&m->waiters, qlink);
69
70
        // Release the internal spinlock
71
        release(&m->splock);
72
```

73

## deadlock.txt Sep 25, 24 9:47 Page 1/3 Deadlock examples 1. Simple deadlock example acquire(mutexA); acquire (mutexB); // do some stuff 9 10 release (mutexB); 11 12 release (mutexA); 13 14 T2: 15 acquire (mutexB); 16 acquire (mutexA); 17 // do some stuff 18 19 release (mutexA); 20 21 release (mutexB); 22

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23 2. More subtle deadlock example
       Let M be a monitor (shared object with methods protected by mutex)
       Let N be another monitor
26
27
28
       class M {
            private:
29
30
               Mutex mutex_m;
31
                // instance of monitor N
32
                N another_monitor;
33
34
                // Assumption: no other objects in the system hold a pointer
35
                // to our "another_monitor"
37
38
            public:
               M();
39
                ~M();
40
41
                void methodA();
                void methodB();
42
43
       };
44
45
       class N {
           private:
46
                Mutex mutex_n;
48
                Cond cond_n;
49
                int navailable;
50
            public:
51
52
               N();
                ~N();
53
54
                void* alloc(int nwanted);
55
                void free(void*);
56
57
59
       N::alloc(int nwanted) {
60
            acquire(&mutex_n);
            while (navailable < nwanted) {
61
                wait(&cond_n, &mutex_n);
63
64
            // peel off the memory
65
66
67
            navailable -= nwanted;
            release(&mutex_n);
68
69
70
71
       N::free(void* returning_mem) {
72
73
74
            acquire(&mutex_n);
75
            // put the memory back
76
77
            navailable += returning_mem;
78
79
            broadcast(&cond_n, &mutex_n);
81
82
            release(&mutex_n);
83
```

```
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                                                                                     Page 3/3
         void
        M::methodA() {
 86
             acquire(&mutex_m);
 88
 89
 90
             void* new_mem = another_monitor.alloc(int nbytes);
 91
             // do a bunch of stuff using this nice // chunk of memory n allocated for us \,
 92
 93
             release(&mutex_m);
 95
 96
 97
        void
 99
        M::methodB() {
100
101
             acquire(&mutex_m);
102
103
             // do a bunch of stuff
104
105
             another_monitor.free(some_pointer);
106
107
             release(&mutex_m);
108
 109
        QUESTION: What's the problem?
110
```