

# Time, liveness & Safety

Last class

- What, how & why.

- The asynchronous model

  - No bounds on MESSAGE DELAYS  
(time b/w send & receive)

  - No bounds on PROCESSING DELAY

  - No global clocks

- Network & FAIRNESS

- Message passing

TODAY ◦ How to REASON about PROTOCOLS (ALGORITHMS) designed for the ASYNCHRONOUS MODEL.

REASONING ABOUT PROGRAMS

- Is it correct?

- What outputs can it produce?

- How long does it take (to execute some ops)?

How MANY MESSAGES DOES IT SEND (...)?

- IS IT FAULT TOLERANT?

- WHAT CONSISTENCY GUARANTEES DOES IT PROVIDE?

...

```
int i = 0; ①
while (true) { ②
    if (i % 7 == 0 || i % 2 != 0) { ③
        print(i) ④
    }
    i++; ⑤
}
```

Ⓐ PRINT ONLY ODD NUMBERS

Ⓑ PRINTS IN ASCENDING ORDER

```
swap(x, y) {
    x = x ^ y
    y = x ^ y
    x = x ^ y
}
```

Ⓐ  $x_{out} = x_{in}; y_{out} = y_{in}$

OBSERVATION: CORRECTNESS OFTEN DEPENDS ON

ORDER OF OPERATIONS

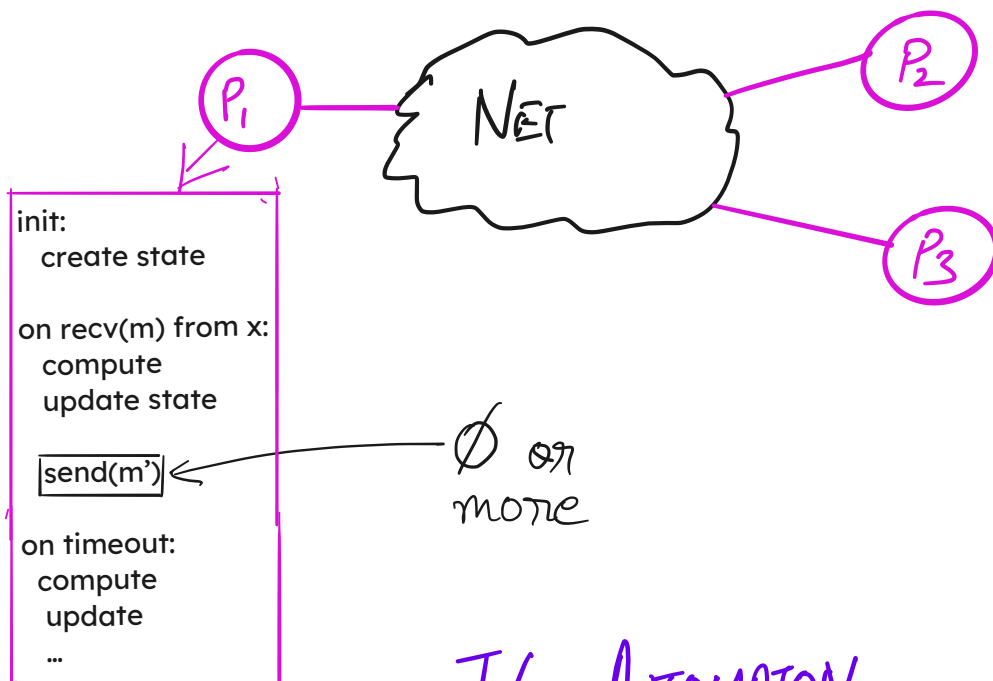
TODAY

Q1 MODELING ORDER OF EVENTS IN DISTRIBUTED SYSTEMS :- TRACES

Q2 CORRECTNESS IN TERMS OF TRACE PROPERTIES  
SAFETY — LIVENESS

Q3 RECOVERING TRACES FROM ACTUAL EXECUTIONS

BUT FIRST: NEED TO ADD SOMETHING TO MODEL

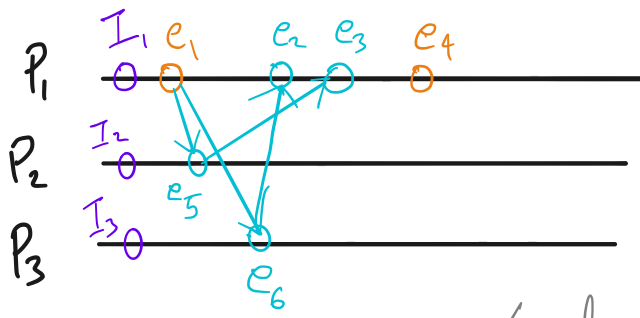


I/O AUTOMATON

(Nancy Lynch & Others)

$e_6 \leq e_5$   
 $e_1 \leq e_2$

$e_5 \rightarrow e_6$



# ORDER OF EVENTS ON ONE PROCESS

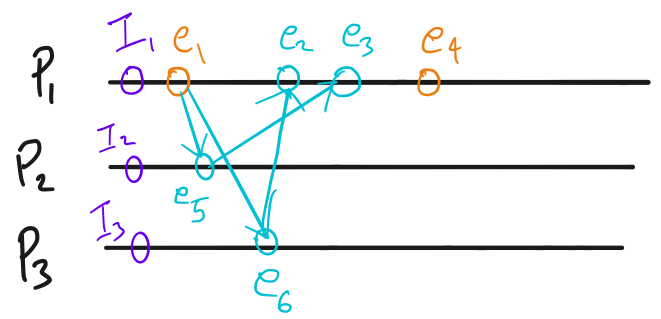
- $\rightarrow (I_1, e_1); (P_1, e_2) \dots$
- $(I_2, e_5); (e_1, e_5)$
- $(e_5, e_3) \quad (e_1, e_6)$
- $(e_6, e_2) \quad (e_2, e_3)$

Local to  $P_1$

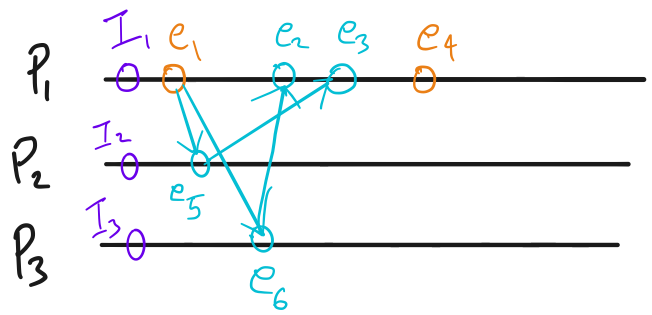
Partial Order

CAUSALITY

# PARTIAL ORDER



# TOTAL ORDER



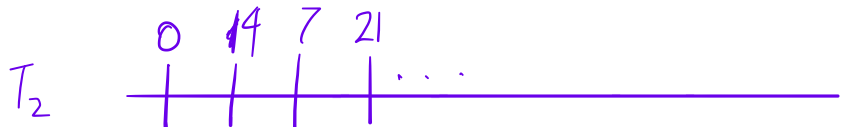
## TRACE

$\mathbb{Z}^+ \rightarrow \text{event } t$

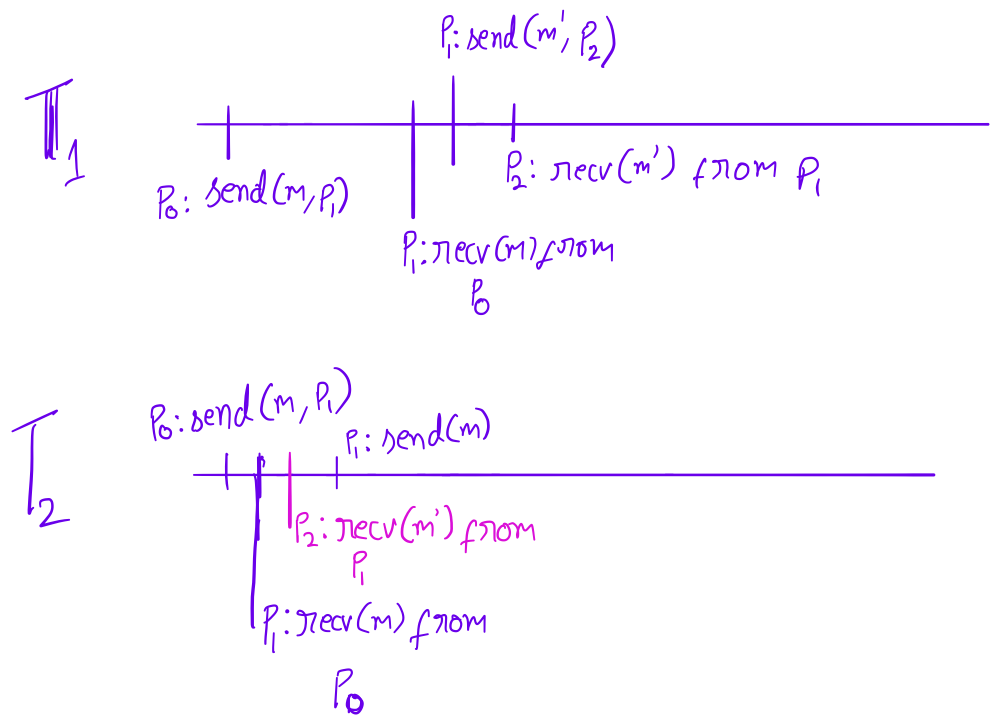
## CORRECTNESS AS A TRACE PROPERTY

Correctness conditions restrict what traces can be produced by the program

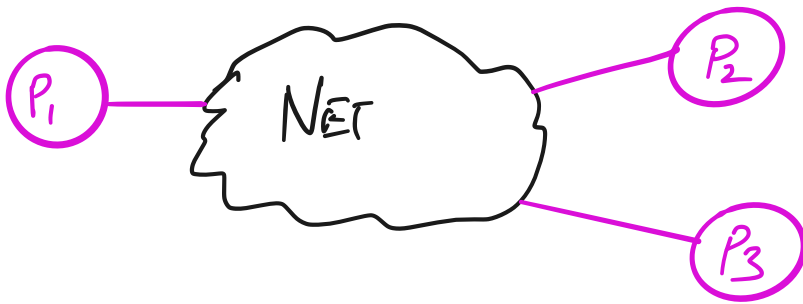
⑥ PRINTS IN ASCENDING ORDER



No messages are received without being sent first

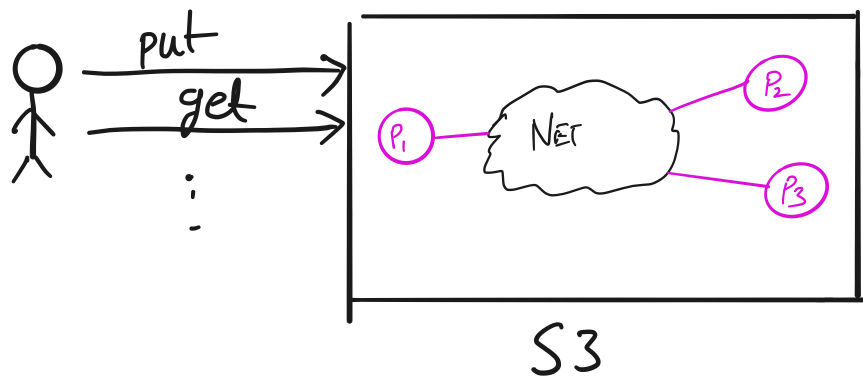


Why look at correctness this way?



- Concurrent processes

- Often visible to the rest of the world as one system



- Correctness is all about what

states anyone outside can observe

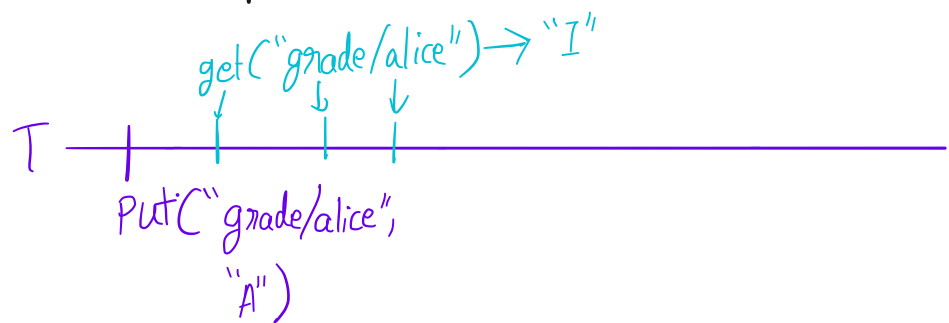
## CORRECTNESS INVARIANTS

SAFETY: CONDITION SHOULD ALWAYS HOLD.

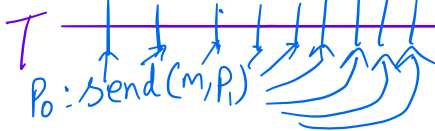
- No recv before send.
- No even number is printed

LIVENESS: CONDITION WILL EVENTUALLY HOLD

- Puts to S3 will eventually update val



- A message sent infinitely often will be received infinitely often



Test: Give a trace  $T$  can you find an extension (SUFFIX)  $S$ , s.t.

$TS$  is admissible  
+ meets liveness condition

Reading: Liveness, uniform liveness, absolute liveness

- Liveness. Given  $T$ ,  $\exists S$  s.t.

$TS$  is admissible & the liveness condition holds

- Uniform  $\exists S$ , s.t.  $\forall T$ , if  $T$  is admissible

$TS$  is admissible & liveness condition holds

$T$  that is admissible

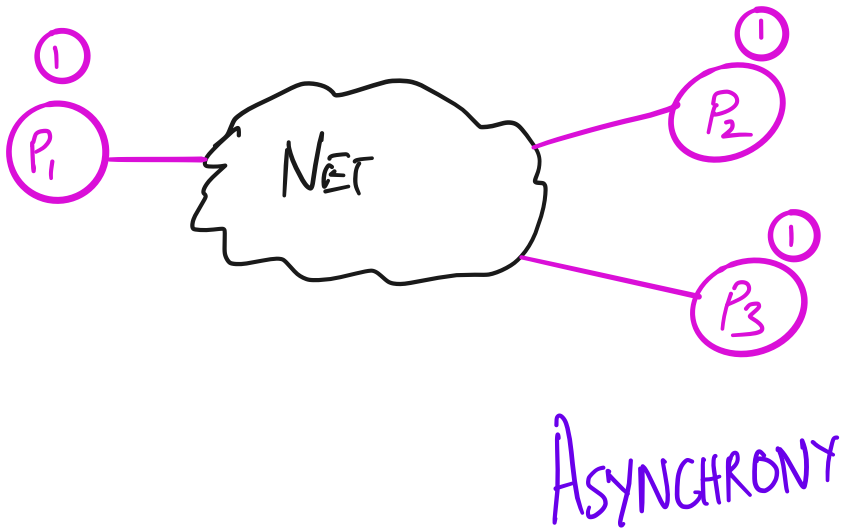


- Absolute: Given a trace  $T$  that is admissible  
 for which the LIVENESS CONDITION holds  
 $\Delta T'$  an admissible trace

$T \cdot T'$  is admissible +  
 liveness holds.

Clocks:

## RECOVERING TRACES IN REALITY



ASSUMPTIONS:

- No global clock
  - Processes have local clocks but
- (a) Run at unknown freq.
- (b) freq. differs b/w processes

What is desired: CAPTURE CAUSALITY

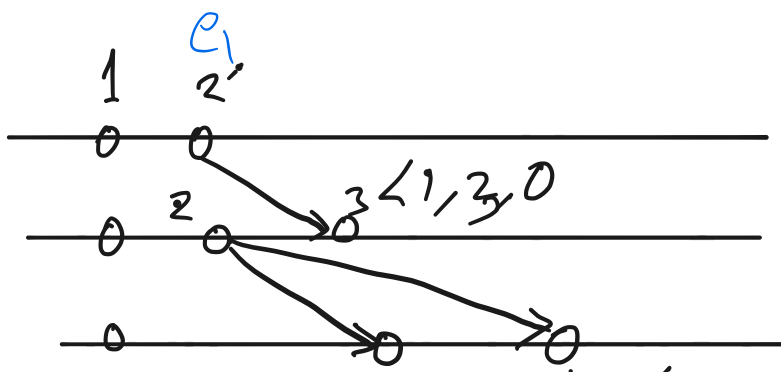
- Lamport's approach

- Vector clocks

Where Chandy-Lamport fits in?

assert-

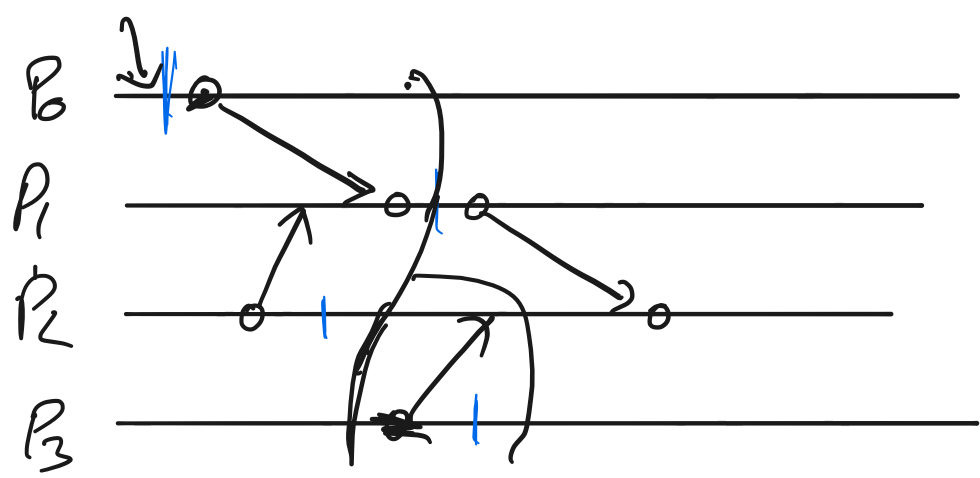
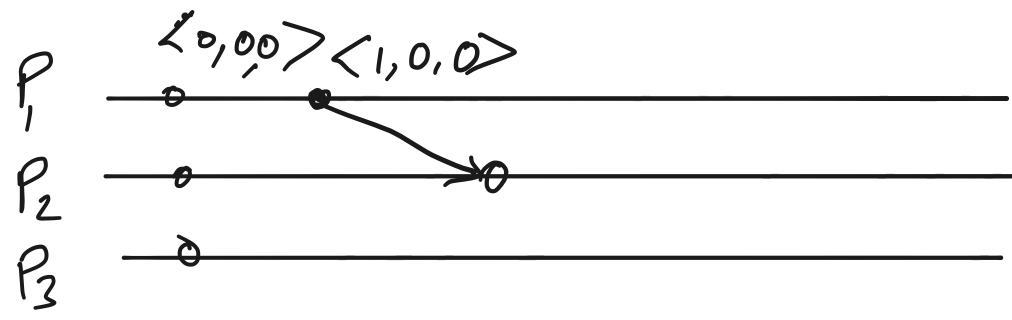
int x  
recv m  
x++



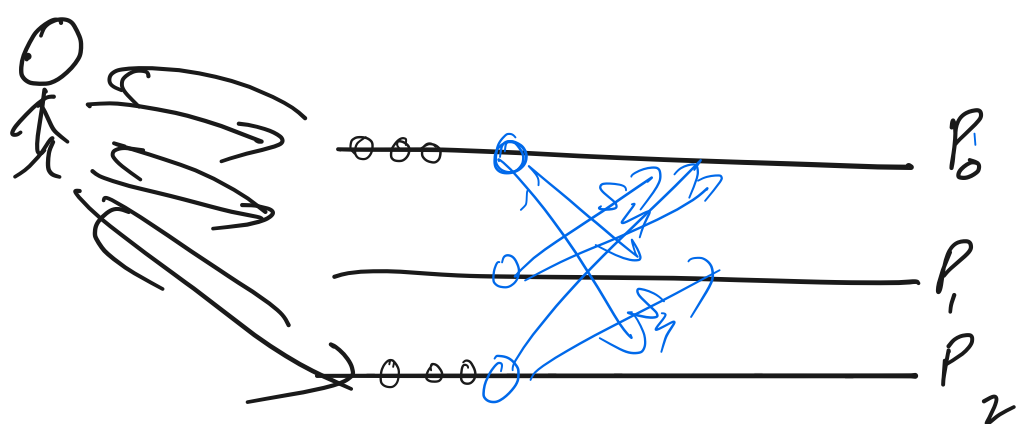
3

4  $\langle 0, 2, 2 \rangle$

1



Causally  
Consistent  
Snapshot

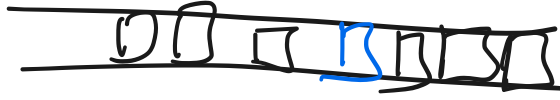


$m_1$   
 $m_2$

Key to give ordered

# Channels with blocks

$P_0 \rightarrow P_2$



$P_1 \rightarrow P_2$

