Question 1
b) and c)
- Some students think it depends on the size of cache. If the whole array of structures can be fitted in the cache, there is no cache missing.
- Few students talk about the memory alignment problem. They want to pad some bits to the end of the array of structure.

Question 3
b)
- One student think only one thread is good enough because of the communication overhead.
- Some students think it needs to check all number between 2 to n-1.

Question 4
- Some students talk about cache missing or cache size.
- Some students think it should pre-load the data in one shot instead of loading it as several separate loads.

Question 5
- Some students don't compare the importance of latency and hit rate among the caches.
- Some students think they are all increases from L1 to L3 caches.